

# SCOTT FABINI

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## EXPERIENCE

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### Senior Software Engineer - Customer Engineering

Jan 2024 - Present

*Adobe*

*Portland, OR*

- Led the Aurora migration, pruning 30TB+ and 20B+ rows and cutting application UI downtime from 1000+ minutes to a 99.9% availability SLA for AWS, our largest customer
- Designed a Splunk-based Performance Tiers system generating \$10M in first-year revenue and enabling SRE to properly provision resources for large subscriptions
- Drove the enhancement request for a Smart Campaign POST API extending rules and flow-step support, positioning the platform for today's agentic workflow automation via LLM Skills and MCP
- Developed a Claude Skill automating enhancement-request submission through Jira, used to process 26 team requests
- Compiled CSO and RCA data to defend against \$100K+ per-incident service credit claims

*Software Engineer*

*Jan 2020 - Jan 2024*

- Documented system design for a Java Spring Boot microservices suite (Notifications, User Audit, Metrics) for streaming events over Kafka via Adobe I/O, enabling 10 enterprise customers to audit platform activity and monitor usage
- Benchmarked MySQL scalability across Google Cloud and AWS Aurora for the CTO, achieving 80K queries/second using sysbench and Lua scripts, confirming a cloud migration was viable at scale
- Served as Customer Lead for AWS, driving dozens of RCAs and enhancement requests as the primary embedded customer-facing technical point of contact, contributing to a \$2.5M engagement

*Associate Software Engineer*

*May 2017 - Jan 2020*

- Reduced navigation tree load time from 20 seconds to under 2 seconds for our largest customer at the time, Microsoft, by using browser tools to identify a memory-inefficient deserialization step in front-end page load
- Extended the Smart Campaign API to support GET queries on rules and flow steps, laying groundwork for customer-built campaign automation via API

### Product Line Manager

July 2010 - July 2014

*Radisys*

*Portland, OR*

- Developed business cases for 4 generations of Intel Core/Atom processors in small form factor motherboards, driving revenues ranging from \$3M-\$15M
- Contributed to the PICMG standards committee defining the COM Express hardware specification

### Systems Architect

May 2000 - May 2010

*Motorola Computer Group*

*Tempe, AZ; Shenzhen, China*

- Defined hardware architecture requirements for 40GbE routers, dual-socket compute blades, power supplies and backplanes for 99.999% high-availability telecom products
- Led hardware architecture for a 15-engineer design team in Shenzhen, China, translating product marketing requirements for MicroATX and credit-card-sized motherboard designs on Intel and Freescale processors
- Co-inventor, US Patent 7,516,263: Re-configurable PCI-Express switching device (2009)

## TECHNICAL SKILLS

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**Languages:** Java, Python, TypeScript, JavaScript, SQL, Lua, PHP, HTML/CSS, C/C++

**Frameworks & Cloud:** Spring Boot, React, Next.js, Tailwind CSS, REST APIs, Terraform, AWS, GCP

**Data, Streaming & Testing:** Kafka, Solr, Mockito, Jest, sysbench, JMeter, MySQL

**Developer & AI Tools:** Git, Docker, CI/CD, Jenkins, Cursor, IntelliJ, Postman, Jira, Claude, MCP

**Monitoring Tools:** Splunk, New Relic, Grafana, Percona Toolkit, VividCortex

## EDUCATION

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### Portland State University

Portland, OR

*Master of Science in Computer Science*

*September 2014 - March 2017*

### Purdue University

West Lafayette, IN

*Bachelor of Science in Electrical Engineering*

*August 1995 - September 2000*